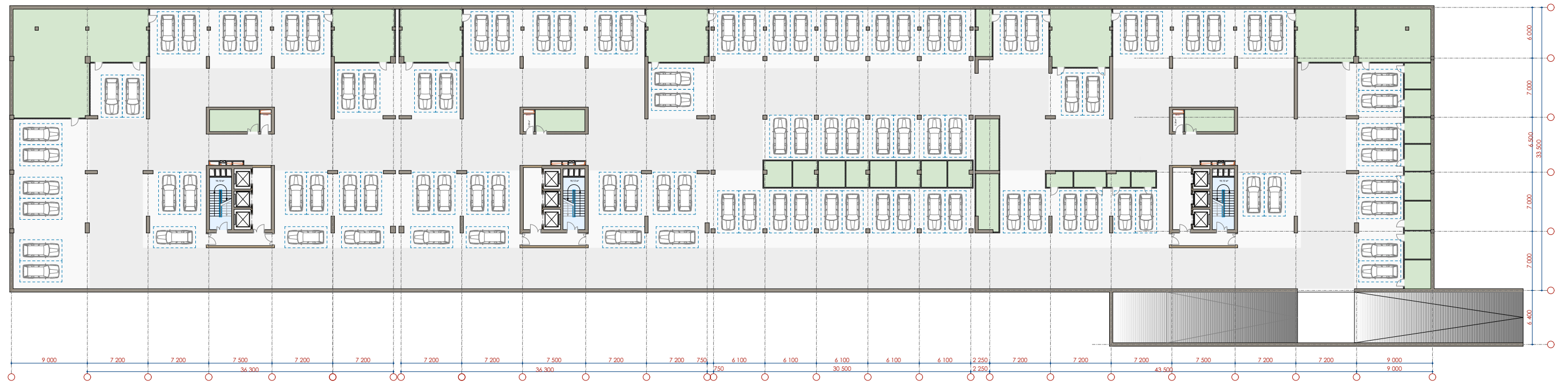
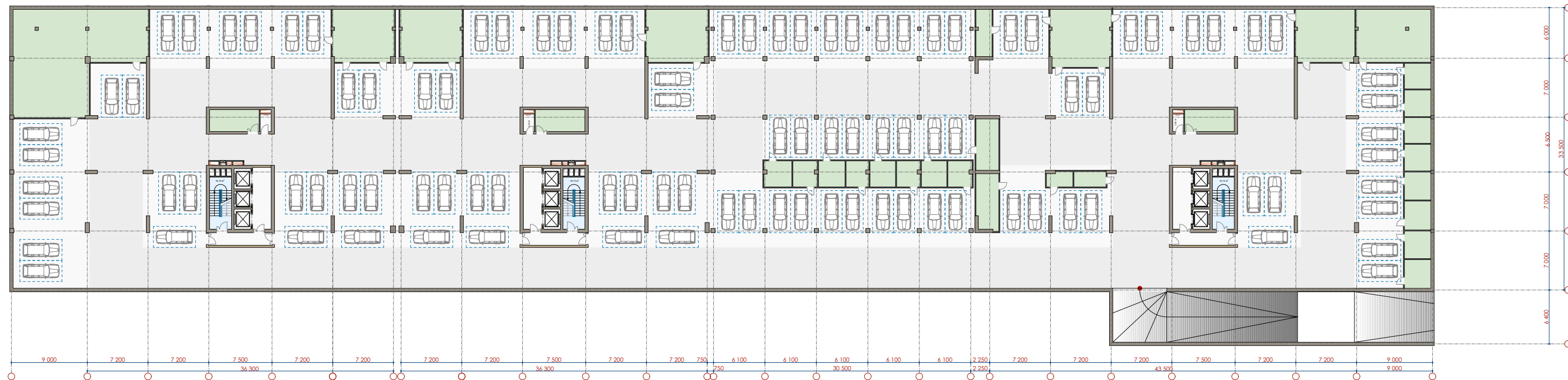


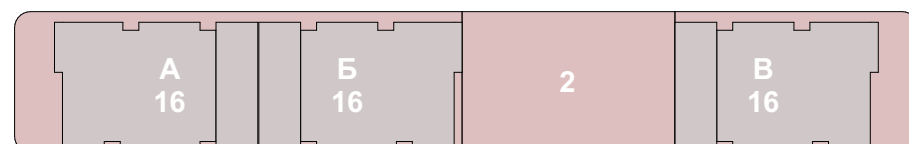
Тарҳи ошёнаи -1 ум



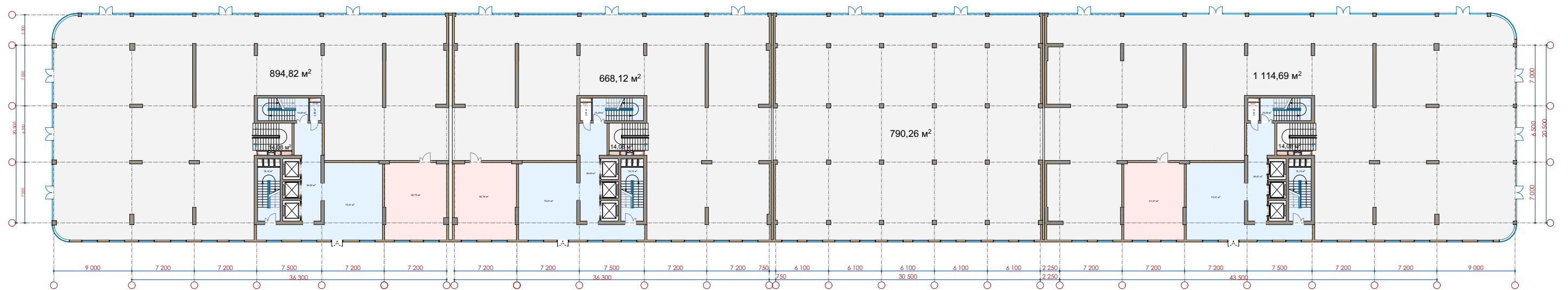
Тарҳи ошёнаи -2 юм



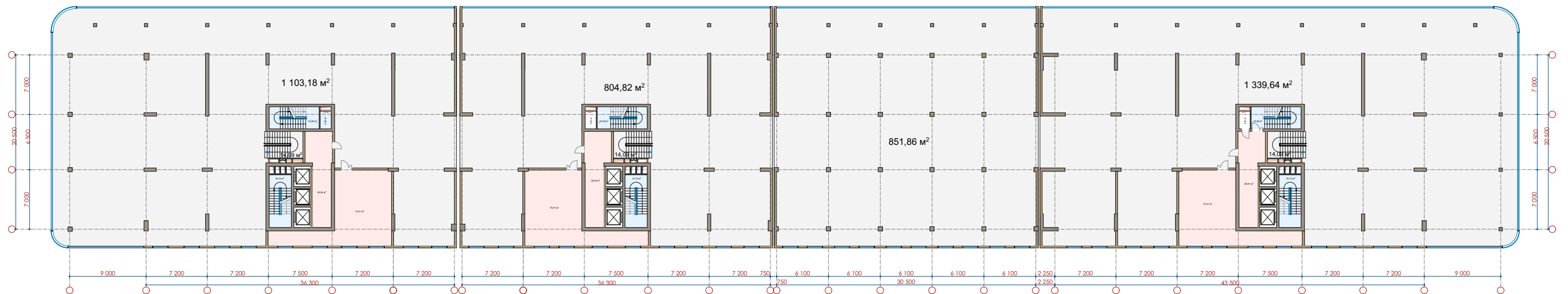
Нақшаи блокбандӣ

[illegible]

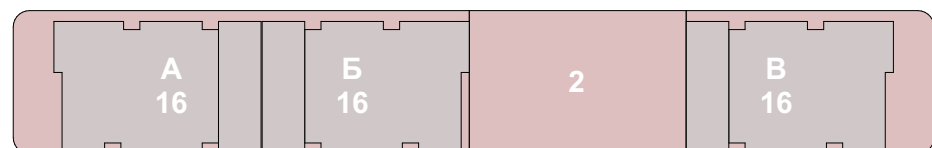
Тарҳи ошёнаи якум

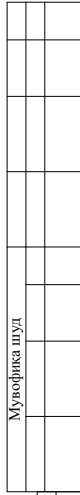


Тархи ошёнаи дуюм

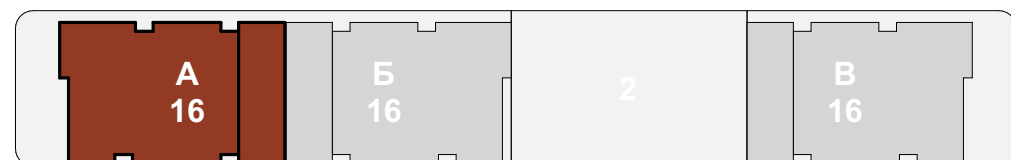


Нақшаи блокбандӣ

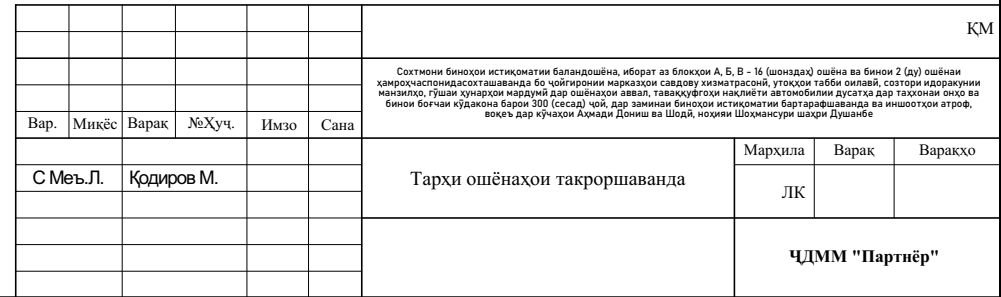
[illegible]

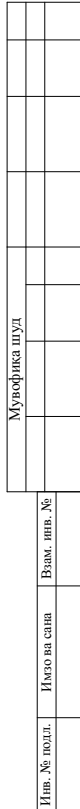


Инв. № подл.	Имзо ва сана	Взам. инв. №
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[illegible]

Инв. № подл.	Имзо ва сана	Взам. инв. №





The diagram shows a 16-bit register divided into three sections. The left section is labeled 'A' and contains the value '16'. The middle section is labeled '2' and contains the value '2'. The right section is labeled 'B' and contains the value '16'. The sections are separated by vertical lines.

[illegible]